

POLYN Highlighting Live NASP Voice Chip Demo at Embedded World 2026

Embedded World 2026, Hall 4, Booth 4-664 – March 9, Nuremberg, Germany - POLYN Technology, a pioneer in ultra-low-power neuromorphic computing, will present a live demonstration of its [NeuroVoice](#) voice processing chip at Embedded World 2026, showcasing the first production-oriented implementation of its silicon-proven Neuromorphic Analog Signal Processing ([NASP™](#)) technology.

The live demo highlights POLYN's first silicon-proven NASP VAD chip, which performs always-on voice detection with microwatt-level power consumption. By executing neural-network inference directly in the analog domain, the chip eliminates the traditional power–latency tradeoff that constrains digital edge AI architectures. The result is deterministic, ultra-fast voice detection suitable for battery-operated and energy-harvesting systems.

The NeuroVoice demo includes additional NASP core models to demonstrate how the VAD core integrates with other voice processing products, such as speaker recognition and voice extraction. NeuroVoice cores support voice interfaces, mission-critical communications equipment, industrial control systems, robotics, and automotive platforms.

“Embedded systems designers are under constant pressure to reduce power budgets while maintaining real-time responsiveness,” said Aleksandr Timofeev, CEO of POLYN Technology. “Our NASP platform enables neural inference exactly where it matters most - at the sensor - delivering intelligent processing without the traditional energy overhead. This demonstration highlights a practical path toward ultra-low-power, voice-triggered devices. Over 2026, we plan to roll out a complete family of voice-processing solutions, including voice enhancement, speaker recognition, and voice control. “

The NeuroSense VAD Evaluation Board will be featured in the Dev Kit Zone at the Embedded Computing Design booth 1-500, Hall 1. Timofeev will personally present the technology and explain the NeuroVoice VAD chip architecture in the Dev Kit Zone on Wednesday, March 11, at 15:30, and visitors will have an additional opportunity to discuss practical implementation aspects of NASP technology for voice edge-AI devices.

At Embedded World 2026, POLYN will also share information on NeuroVoice evaluation kits, integration models for OEMs, and early-access programs for companies developing ultra-low-power voice-enabled products.

With NASP technology, neural processing moves from high-power digital systems to energy-efficient analog computation at the edge - redefining what is possible in embedded AI design.

About POLYN

POLYN Technology Ltd. is a fabless semiconductor company that develops Neuromorphic Analog Signal Processing (NASP™) technology and products, bringing AI to the sensor edge with unprecedented energy efficiency and ultra-low latency. POLYN has offices in the UK, Israel, the US, Europe, and Asia.