

# CADY

## CADY 4.0 Capabilities



### Connection Instruction Violations

- Direct Connection Instruction Violation
- Floating Logic Pin
- Reversed Polarity
- Missing Decoupling Capacitors
- Activity Level Errors
- Common Component Connection Errors  
Supported: Buffers, Relays, Transistors, Crystals, LEDs, TVS Diodes, Operational Amplifiers



### Communication Protocol Validations

- Protocols Connections Violation  
Supported: I2C | SPI | UART | RS-485/232 | USB | I2S | Ethernet | CAN | SMBus | QSPI | MIPI | PCIe | JTAG | LVDS | HDMI
- Differential Lines Mix-Up
- Termination Errors
- Standard Connectors Misapplication
- DDR Mix-Up (Q2)



### Deep Voltage Analysis

- Supply Voltage Out of Operating Range
- Voltage Regulators Output Verification
- Potential Capacitor Voltage Breach
- Incompatible Rail / Pull-Up Voltage
- Incompatible Logical Levels (Q2)
- Power-to-GND Excessive Leakage
- Resistor Overpower (Q2)



### Best Practice Recommendation

- Redundant Pull Resistor
- Test Point Coverage
- Capacitor on Reset Pin
- Enable/Gate Pin Pull-Down
- MOSFET Biasing Resistor
- Net Naming Conventions Enforcer (Q2) ★
- Additional Custom Rules Upon Request ★



### Additional Capabilities

- FPGA / MCU Pin Assignment Interface ★
- Multi-Board Analysis ★
- SOM Wizard ★
- Operating Ambient Temperature Range
- MPN vs. Footprint Compatibility Check
- Component Supply Lifecycle Status and Chain availability ★
- Component Compliance Standards (ROHS, AECQ, etc.) ★